Multi-physics design optimization of a GaN based   
integrated modular motor drive system

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Abstract

In this paper, a multi-physics approach is presented for the design optimization of an integrated modular motor drive (IMMD). The system is composed of a modular permanent magnet synchronous motor and a GaN based modular motor drive power stage. The multi-physics model includes motor drive inverters and DC link capacitor bank (electrical model), stator windings and rotor magnets (electromagnetic model), heat sink (thermal model) and a geometrical model. The main purpose of the design optimization is to obtain the highest power density possible, which is quite critical in integrated drives. Due to the integrated structure, the system has several interdependencies and parameters are selected based on those relationships. An 8 kW IMMD system design is proposed and the resultant system design is verified using various simulation platforms.

1 Introduction

Conventional variable frequency motor drives are composed of two distinct parts: drive and motor, where the drive unit(s) are placed in separate cabinets and connected to the corresponding motors with long cables. This reduces the power density, increases cost and causes electromagnetic interference (EMI) problems [1]. In integrated modular motor drive (IMMD) systems, the drive is integrated onto the motor back iron forming a single package such that the power density of the overall system is increased and the connection cables are eliminated [1]. Furthermore, each pole of the motor is driven by its own drive module which are then interconnected via a common DC link. By doing so, the fault tolerance of the system is increased, heat dissipation is spread on a wider surface area and voltage stress on windings and power semiconductor devices are reduced [2].

In IMMDs, the space available for the drive system components is drastically reduced due to integration. Therefore, fitting all the components requires design optimization with integrated model approach and careful spatial and layout design. Moreover, the interdependencies between the main system components yields a multi-physics approach where the design of motor, drive power electronics and thermal management system should be considered all together. Therefore, it is highly difficult to propose a decoupled design approach in integrated drives as one parameter may affect the other significantly.

The current IMMD prototypes proposed in the literature are usually based on the new generation wide band-gap power semiconductor devices, such as Gallium Nitride (GaN) power FETs [3]. These devices are capable of switching at much higher switching frequencies compared to their silicon counterparts thanks to their low switching losses [3]. It is possible to reduce the size of passive components with high switching frequencies as well as reduce the size of heat sink with superior efficiency values with the utilization of enhancement mode (e-mode) GaNs. Considering that the largest components on an average power converter system are passive components and the heat sink [1], utilization of these devices is critical for IMMD designs. Moreover, thanks to the modularity of the system, interleaving technique can be used to further reduce the size of DC link capacitor bank [4]. Although several studies have been published regarding power electronics design, modular inverter topologies, DC link capacitor selection etc. for IMMDs [1,3,5], most of these prototypes lack a unified design procedure. One example for such a dependency is that, the cross-sectional area available for the motor drive printed circuit board (PCB) and heat sink is determined by the diameter of the motor.

In this paper, the optimum design of an 8kW IMMD system is presented considering both the motor and drive parameters to obtain the highest power density. It is also aimed to maximize the overall system efficiency while keeping the active material costs in acceptable limits. A permanent magnet synchronous motor (PMSM) having fractional slot concentrated winding (FSCW) stator is utilized for its superior torque density and fault tolerance capability which makes it suitable for IMMD applications [6]. The content of the multi-physics design optimization approach includes electrical, electromagnetic, thermal and geometrical models. First, the multi-physics model is presented in Section 2. In section 3, the basic relations between the system parameters and the cost and constraint functions are obtained and the inter-dependencies are discussed. Using the results of Section 3, an optimum system design is proposed in Section 3. In Section 4, the presented models are verified and the proposed system design is evaluated using simulation results. The paper is concluded in Section 5.

2 System modelling

Motor drive integration onto the modular stator back iron is considered in this paper, an example of which is shown in Fig. 1 [5]. The structural configuration of the proposed system is also shown in Fig. 2.

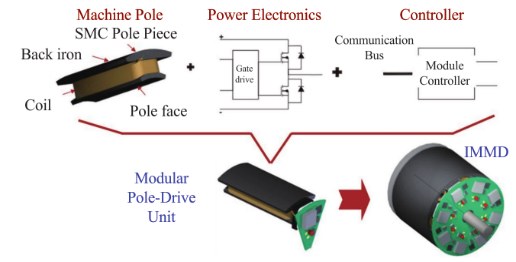


Figure 1. An example of the IMMD structure [5]



Figure 2. Structural configuration of the proposed IMMD

The main blocks of the system model and the relations between different aspects used in the integrated design are shown in Fig. 3. Basically, the system is composed of the electrical model, electromagnetic model, thermal model and geometrical (spatial) model. The system specifications and constraints are listed in Table 1. Moreover, the independent variables and their corresponding sub-models are also shown in Table 2. The IMMD system has a modular structure where each three-phase inverter module drives its own part of the stator pole. The modules can be connected in series and/or parallel configuration via a common DC link. A block diagram of the system is shown in Fig. 3 with 2-series and 2-parallel connected modules.



Figure 3. Main blocks of the system model

|  |  |
| --- | --- |
| **Parameter** | **Value** |
| DC link voltage, *Vdc* | 540 V |
| Number of phases in each module, *m* | 3 |
| Motor total output power, *Pout* | 8 kW |
| Motor rated speed, *Nr* | 600 rpm |
| Machine electric loading, *Arms* | 35 kA/m |
| Machine magnetic loading, *Bavg* | 0.6 T |
| Maximum winding current density, *Jrms* | 4 A/mm2 |
| Maximum stator teeth flux density, *Bts* | 1.8 T |
| Maximum stator yoke flux density, *Bys* | 1.4 T |
| Maximum fill factor, *kcu* | 0.6 |
| Maximum device junction temperature, *Tj-max* | 150 0C |
| Maximum capacitor temperature, *Tcap-max* | 70 0C |
| Maximum DC link voltage ripple, *Vdc-r* | 1 % |
| Minimum motor efficiency, *ηm-min* | 96 % |
| Minimum drive efficiency, *ηd-min* | 98 % |
| Minimum power factor, *cos(φ)min* | 0.9 |
| Ambient temperature, *Tamb* | 50 0C |

Table 1. System specifications and constraints

|  |  |
| --- | --- |
| **Optimization parameters** | **Sub-model** |
| Number of modules, *n* | *Electrical* |
| Number of series modules, *ns* | *Electrical* |
| Switching frequency, *fsw* | *Electrical* |
| Modulation index, *ma* | *Electrical* |
| Aspect ratio of the motor, *α* | *Geometrical* |
| Slot/module/phase, *ws* | *Electromagnetic* |

Table 2. Optimization parameters (independent variables)



Figure 3. A general block diagram of the IMMD

2.1 Electrical sub-model

The electrical model selects rated parameters of the motor drive inverter, power semiconductor devices, calculates motor drive losses and required DC link capacitor parameters. A set of 650V e-mode GaN FETs suitable for high voltage applications having different current ratings from GaN Systems are used for the design [7]. The devices are selected from these commercial products based on the number of series and parallel modules (*ns, np*), modulation index (*ma*), power factor (*cos(φ)min*) and motor efficiency (*ηm-min*) limitations. By using the selected device parameters, the motor drive losses are determined as shown in (1) for forward conduction loss, (2) for reverse conduction loss, (3) for switching loss, where *Eon*, *Eoff* and *Eoss* are the on state, off state and output capacitance switching energies, respectively, *Rds-on* is the on state resistance, *Ip* is the peak line current.

|  |  |
| --- | --- |
|  | (1) |
|  | (2) |
|  | (3) |

The analytical models for the determination of required capacitance (*Cdc*) and RMS current rating (*Ic-rms*) for a typical inverter are given in (4) and (5), where *Irms* is the rms line current [4]. For a modular motor drive, it is possible to use interleaving technique to reduce these requirements. The effect of interleaving is determined by proper phase shifting angle for each possible case and added to the capacitance and ripple current requirements. It has been shown that series connection has no effect on the ripple current and ripple voltage for any phase shifting angle. The normalized effect of interleaving on these parameters is shown in Fig. 5.

|  |  |  |
| --- | --- | --- |
|  | (4) |  |
|  | (5) |  |



Figure 5. The effect of interleaving for parallel connected modules

A database of film capacitors are used from commercial products [8] for capacitor selection model which is based on the capacitance, ripple current and DC voltage requirements. The selected capacitor is checked whether its core temperature exceeds the specified limit or not, by using the implicit thermal model expressed in [8] and capacitor datasheet values such as ESR, thermal conductance etc. Temperature dependency is especially critical since it affects the lifetime of the capacitors significantly. The required phase induced voltage per module (*Ephm*), which is the link between the electrical and electromagnetic models is determined using the inverter model as in (6).

|  |  |
| --- | --- |
|  | (6) |

2.2 Electromagnetic sub-model

This section summarized the electromagnetic design of a PMSM for the IMMD application. The number of stator slots (*Qs*) is determined by using *ws, m* and *n*. The machine stator structure is FSCW as it is more suitable for high torque modular motor applications thanks to its high power density and torque density, low cogging torque, low manufacturing cost and fault tolerance capability. The number of poles (*p*) is determined for each possible *Qs* to get low cogging torque while keeping the winding factor high enough and harmonic content at minimum. Tables with pre-determined winding factor values (*kw*) for different *Qs*/*p* combinations are used for winding factor [6].

The air gap distance (*lg*) is found using the target peak air gap flux density (*Bgp*) and the properties of the selected magnet (*NeFeB*) as in (7), using the lumped parameter magnetic circuit model shown in Fig. 6, where *lm* is the magnet length. The same methodology is used for the determination of tooth width (*bt*) and back core height (*hbc*) using the maximum allowable flux density (*Bts-max*and *Bys-max*), as in (8) and (9), where *em* is the magnet embrace and *Dis* is the bore diameter.

(a) (b)

Figure 6. (a) Electromagnetic lumped parameter magnetic circuit, (b) Slot dimensions

|  |  |
| --- | --- |
|  | (7) |
|  | (8) |
|  | (9) |

The determination of the number of turns is based on *Ephm* and flux per pole (*Φpp*). *Φpp* is found using pole area and flux density. Induced voltage on one turn is determined as in (10), where *La* is the axial length and *fr* is the mechanical frequency. The number of turns per coil side (*Ncs*) can be found using (11), where *l* is the number of layers. The phase current of each module is found by assuming that the motor is operated in vector control mode where the induced voltage is always in-phase with the current, as in (12). The electric loading of the machine is verified using the rated phase current as in (13). Once the rated current is established, winding is selected from standard AWG wires with the specified current density limit (*Jrms*). The only remaining parameter on the machine dimensions is the slot height (*hs2*) as shown in Fig. 6. The limiting factor for *hs2* is the maximum slot fill factor (*kcu-max*), as seen in (14). Now, all the dimensions of the machine are set including the stator outer diameter (*Dos*) which is critical for power density.

|  |  |
| --- | --- |
|  | (10) |
|  | (11) |
|  | (12) |
|  | (13) |
|  | (14) |

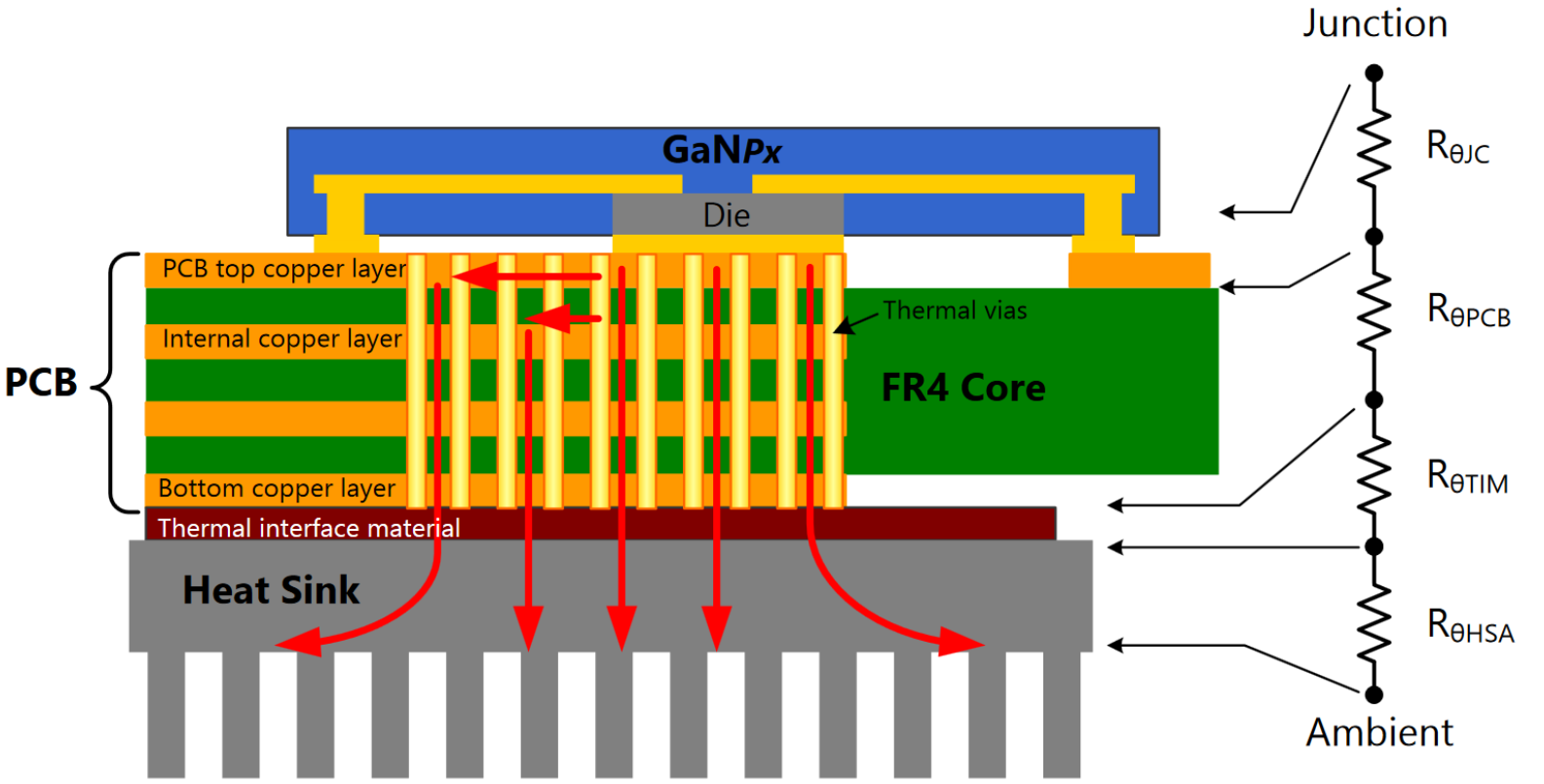
Once all the machine dimensions, winding configuration and turn numbers are set, the machine losses are calculated for the evaluation of the design. Copper loss is directly related to the selected winding cross-section (*Awdg*) and the mean-length-turn as expressed in (15). The temperature effect is added via the temperature coefficient of copper, where *ρcu* is the resistivity of copper for a given temperature. For core loss, the selected lamination (*M250-50A*) is used in several FEA simulations to determine the core loss density with worst case flux density values in several parts of the core. A core loss density of *4 W/kg* is obtained including fundamental and harmonic components and used for core loss calculation.

|  |  |
| --- | --- |
|  | (15) |

2.3 Thermal sub-model

The thermal model including GaN devices and the lumped parameter thermal circuit used at steady state to determine the maximum heat sink thermal resistance (*Rth-sa*) are shown in Fig. 7. Natural cooling is preferred in the design due to the reliability issues. *Rth-sa* is calculated as (17) to ensure that the junction temperature of any device do not exceed their maximum values (*Tjmax*). Thermal resistance of PCB and thermal interface material (TIM) are determined using manufacturer’s application note [7]. Analytical models are used to determine heat sink dimensions and fin geometry. The fin geometry and physical model of the heat sink is shown in Fig. 8. The thermal resistance of the heat sink is expressed in (18) for natural convection, where *Abase* is the base plate area, *Afin* is the total fin surface area, *ηfin* is the fin efficiency and *Nfin* is number of fins. The convection coefficient (*h*) is found using an analytical model derived in [9].





(a) (b)

Figure 7. (a) Thermal model structure [7], (b) Lumped parameter thermal equivalent circuit

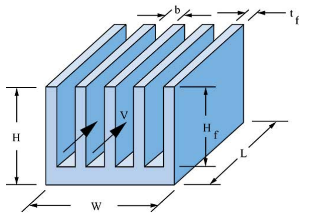


Figure 8. The physical model of the heat sink [9]

|  |  |
| --- | --- |
|  | (17) |
|  | (18) |

2.4 Geometrical sub-model

In the geometrical model, main machine dimensions (*Dis, La*) are determined using torque per unit volume, *Arms* and *Bavg*, as expressed in (19). Electric and magnetic loading values are the target parameters specific to natural cooled PMSMs at this power level. The aspect ratio (*α=La/Dis*) gives the resultant dimensions. After the capacitors are determined, heat sink and other motor parameters, the objective function, the volumetric power density (*PDv*) of the system is calculated using the height of each component and the machine outer diameter as shown in Fig. 2. Moreover, the active material volume and mass for copper, magnet and iron are also calculated.

|  |  |
| --- | --- |
|  | (19) |

3 System evaluation with design parameters

The variables in Table 2 are used to investigate the effect of each design parameter to the system performance indices such as overall system power density, drive and motor efficiency and active material mass.

3.1 The effect of switching frequency

Variation of motor drive efficiency (*ηd*), total required DC link capacitance (*Cdc*), power density (*PDv*), and heat sink (*Vhs*), drive (*Vdr*) and total volume (*Vtot*) with switching frequency (*fsw*) are shown in Fig. 8 for different parallel connected modules (*np*). *ηdr* decreases as *fsw* increases as expected, however this effect is more severe with low *np* values because switching losses become more dominant than the conduction losses as the GaN current rating increases. Moreover, the relation with number of modules and efficiency is not linear because GaN selection is based on discrete commercial devices which causes underutilization for some cases.

(a) (b)

(c) (d)

Figure 8. (a) *ηd*, (b) *Cdc*, (c) *Vhs*, *Vdr* and *Vtot*, (d) *PDv* variations with *fsw*

*Cdc* is inversely proportional to *fsw* as expected. After a certain point, reduction of *Cdc* is not significant, especially for high *np* values. The rms ripple current requirement (*Icrms*) dominates *Cdc* requirement for high *fsw* in capacitor selection, because *fsw* has no direct effect on *Icrms*. It is also expected that *Vhs* increases while *Vdr* decreases with increasing *fsw* values. The effect of *fsw* to the total volume also saturates after a critical point, which is different for different *np* values. This means that, increasing *fsw* does not provide further volume reduction above 100 kHz which is also observed in power density variations. Effect of *np*

on *PDv* also saturates after 4 parallel modules as the effect of interleaving also saturates.

3.2 The effect of modulation depth

Variation of *ηd*, required *Cdc*, *Icrms* and motor efficiency (*ηm*) with modulation depth (*ma*) are shown in Fig. 9 for different *np* values. *ηd* increases as *ma* increases since the same amount of power can be transferred with less current which reduces the conduction losses. However, this effect is not also linear with varying *np* because of the discrete nature of the selected devices. In this analysis, the device selection is based on minimum possible device cost. If this were not considered, very high efficiency values would be obtained, which is not practical. The effect of *ma* on *Cdc* is not as severe as frequency, especially for high *np* values. On the other hand, *Icrms* decreases drastically with increasing *ma*. Normally, increasing *ma* would also reduce efficiency due to increasing current, however there is a similar phenomenon in *ηm* variation with *ηd* variation, which is caused by the discrete winding selection in the model. *ηm* does not vary much with varying *ma*. Similarly, effect of *np* on *ηm* is not critical beyond 2 parallel modules.

(a) (b)

(c) (d)

Figure 9. (a) *ηd*, (b) *Cdc*, (c) *Icrms*, (d) *ηm* variations with *ma*

3.3 The effect of series connection

The variation of *Cdc* and *PDv* for different number of series (*ns*) and parallel (*np*) connected modules are shown in Fig. 10. *Cdc* increases with *ns* linearly, which causes volume and cost increase, although *ns* has no direct effect on *Icrms*. The volume increase can also be observed on *PDv* variation. As a result, high number of series connection is not feasible considering also the increased number of devices.

(a) (b)

Figure 10. (a) Required *Cdc*, (b) *PDv* variation with *ns* and *np*

3.4 The effect of aspect ratio and number of slots

Variation of *ηm*, *PDv* and total motor active material cost (*Cm*), which includes iron ($3/kg), copper ($10/kg) and magnet costs ($80/kg), with aspect ratio (*α*) are shown in Fig. 11 for different *np* values. Number of slots (*Qs*) is directly proportional to *np* as expressed in (k). The first observation from this analysis is that, increasing *Qs* beyond 24 do not have significant effect on any of the performance indices. Moreover, a very high *Qs* is not practical as the slot-pitch gets very thin. Increasing *α* has positive effect on the power density, however this effect vanishes after a critical point, which is also different for different *np* values. On the other hand, increasing *α* decreases *ηm* and increases *Cm* almost linearly, which are both disadvantageous. These results suggest that, each *np* has its own optimum *α*, which is nothing but the aforementioned critical point.

|  |  |
| --- | --- |
|  | (k) |

(a) (b)



(c)

Figure 10. (a) *ηm*, (b) *PDv*, (c) *Cm* variations with *α*

4 Optimum system design

Using the results of Sec. 3, an optimum system design is performed and resulting system parameters are listed in Table 3. In this section, selection of each parameter is explained and the relations between the parameters are discussed. Performance of the designed system is shown in Table 4.

*Number of series connected modules (ns)* is selected as 2, which is the minimum possible value due to the voltage rating of GaNs (650V), since a higher value results in a worse performance in all aspects.

*Modulation index (ma)* is selected as 0.9 since the system performance is not affected significantly for larger *ma* values, especially *Icrms* which is highly dependent on *ma*. There is a possibility of over-modulation, which may increase losses due to injected low order harmonics. Therefore, *ma* should be determined by a margin in case of any voltage sags.

*Number of parallel connected modules (np)* and *fsw* are considered together since their effects are more inter-dependent. *1 parallel* is not feasible for any frequency in all aspects. *ηd* drops below the specified constraint (*98%*) for *3 parallel*, therefore it is not selected although *PDv* is higher than *2 parallel*. *5 parallel* has the highest efficiency, however using *np > 4* is not feasible as the slot pitch gets too small. Between *2 parallel* and *4 parallel*, the latter seems to be more advantageous in terms of, *Cdc, Idc-rms, ηd, ηm*, and most importantly *PDv*, which is 18% higher. However, it has other disadvantages, which do not appear in this analysis, such as drive power stage cost (*Cdr*) and control stage cost, system reliability due to increased complexity and motor manufacturing cost due to increased number of slots. For the GaNs, the *cost vs current* relation is not linear such that, *Cdr* for *2 parallel* is around 70% of *Cdr* for *4 parallel*. In conclusion, *2 parallel* should be selected if cost and reliability are more of concern, however *4 parallel* should be selected if *PDv* is the biggest concern.

*Switching frequency (fsw)* selection depends on the critical point beyond which the power density do not vary significantly. It is not wise to increase *fsw* further as the efficiency reduces. This critical value is different for each *np* such that, it is around 50 kHz for *2 parallel*, while it is around 100 kHz for *4 parallel*.

*Aspect ratio (α)* also has a critical point for each particular *np*. For this design, any value between 0.5 and 0.9 is feasible. Increasing number of slots also has no significant effect beyond 12.

|  |  |
| --- | --- |
| **Designed parameters** | **Value** |
| Number of parallel modules, *np* | *2* |
| Number of series modules, *ns* | *2* |
| Switching frequency, *fsw* | *50 kHz* |
| Modulation index, *ma* | *0.9* |
| Aspect ratio of the motor, *α* | *0.5* |
| Slot/module/phase, *ws* | *2* |

Table 3. Resulting system parameters

|  |  |  |  |
| --- | --- | --- | --- |
| **Parameter** | **Value** | **Parameter** | **Value** |
| Drive efficiency, *ηdr* | *98.3 %* | Power density, *PDv* | *0.71 kW/lt* |
| Motor efficiency, *ηm* | *96.6 %* | Motor cost, *Cm* | *249.5 $* |

Table 4. Performance of the designed system

5 Conclusions

In this paper, a multi-physics design method is presented for a GaN based 8 kW IMMD system. A multi-physics model of the system is developed including an electrical model (motor drive), an electromagnetic model (machine), a thermal model (heat sink) and a geometrical model. The system power density, drive efficiency, motor efficiency and costs are evaluated using the developed optimization tool. A more realistic analysis is performed with real component selection (GaN, capacitor, wire) and inclusion of their costs. Thanks to the discrete nature of the components, design variations are corrected.

Thanks to the modularity of the system, the effect of series and parallel connection is investigated. It has been shown that, a modular motor drive is beneficial compared to conventional motor drives to achieve a more efficient and a smaller system. However, there is a limit to this modularity due to reliability, material cost and manufacturability. Moreover, system performance tend to saturate above some a specific number. There is a trade-off between cooling and passive component size in terms of switching frequency. Each modular system has its own optimum frequency, beyond which further increase does not contribute to any of the performance criteria. Generally, frequencies higher than 100 kHz are not feasible even with GaN devices. The availability of interleaving also contributes to this, thanks to modularity. Furthermore, dc link RMS current is not a high concern thanks to the utilization of film capacitors. It has been shown that, capacitance requirement dominates the current requirement. Therefore, capacitor volumes can be reduced more flexibly with variable switching frequency as well as utilization of interleaving. Aspect ratio is the link between the power electronics and machine sides since the cooling and drive PCB surface area directly depend on it. For a given rating, aspect ratio of the machine, while it is also dependent to modularity.. Furthermore, increasing the number of slots does not contribute to power density if the system is modular.

It has been shown that, a multi-domain design approach is important because of the complex relations between different aspects of the system. In the end, an IMMD with 0.71 kW/lt power density has been obtained, including both motor and the drive, with drive efficiency above 98% and motor efficiency above 96%.

Acknowledgements

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References

[1] G. Lo Calzo, G. Vakil, B. Mecrow, S. Lambert, T. Cox, C. Gerada, M. Johnson, and R. Abebe, “Integrated motor drives: state of the art and future trends,” *IET Electr. Power Appl.*, vol. 10, no. 8, pp. 757–771, Sep. 2016.

[2] M. D. Hennen, M. Niessen, C. Heyers, H. J. Brauer, and R. W. De Doncker, “Development and control of an integrated and distributed inverter for a fault tolerant five-phase switched reluctance traction drive,” *IEEE Trans. Power Electron.*, vol. 27, no. 2, pp. 547–554, 2012.

[3] J. Wang, Y. Li, and Y. Han, “Integrated Modular Motor Drive Design With GaN Power FETs,” *IEEE Trans. Ind. Appl.*, vol. 51, no. c, pp. 3198–3207, 2015.

[4] M. Ugur and O. Keysan, “DC link capacitor optimization for integrated modular motor drives,” *2017 IEEE 26th Int. Symp. Ind. Electron.*, vol. i, pp. 263–270, 2017.

[5] A. Shea and T. M. Jahns, “Hardware integration for an integrated modular motor drive including distributed control,” in *2014 IEEE Energy Conversion Congress and Exposition (ECCE)*, 2014, pp. 4881–4887.

[6] N. Bekka, M. E. H. Zaim, N. Bernard, and D. Trichet, “A Novel Methodology for Optimal Design of Fractional Slot with Concentrated Windings,” *IEEE Trans. Energy Convers.*, vol. 31, no. 3, pp. 1153–1160, 2016.

[7] GaN Systems, “GaN Systems.” [Online]. Available: http://www.gansystems.com/. [Accessed: 15-Jan-2018].

[8] TDK, “Film Capacitors, Metallized Polypropylene Film Capacitors (MKP) - B32674...B32674 Datasheet,” no. May. 2015.

[9] R. E. Simons, “Estimating Parallel Plate-Fin Heat Sink Thermal Resistance.” [Online]. Available: https://www.electronics-cooling.com/2003/02/estimating-parallel-plate-fin-heat-sink-thermal-resistance/.